
DrivExpress Release Notes

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The version of DrivExpress package consists of 3 numbers like 1.0.0. The upper 2 numbers means the version of DrivExpress library. The lowest number ups when any package contents except for DrivExpress library change, for example, documents or sample scripts, etc. The lowest number is reset to zero when the version of DrivExpress library changes.

The version of DrivExpress library consists of 2 numbers like 1.0. The upper number means major version and the lower number means minor version. The major version ups by outstanding new features or modifications which have an impact on previously developed scripts. The minor version ups by trivial new features or modifications which make no impact on previously developed scripts. The minor version is reset to zero when the major version changes.

1 DrivExpress Package 1.3.0 — 2014.01.31

Library Improvements:

- Changed the link initialization sequence to support ALTERA Stratix V, Arria V, and Cyclone V series.

Package Modifications:

- Supported ALTERA Quartus II 13.1.
- Revised the user's manual from Rev.D to Rev.E because of the package modifications.

2 DrivExpress Package 1.2.0 — 2013.06.28

Library Improvements:

- Changed the link event detection command `link_event_wait()` to prevent the registered link monitor function from being called for unused lanes.¹

Package Modifications:

- Changed the DMA transfer size of the sample script to not extend beyond the payload data size limit “4KB” of the starter edition license.
- Added new two packages. One is the Cadence NC-Sim package and the other is the Synopsys VCS package.
- Supported ALTERA Quartus II 13.0.
- Revised the user's manual from Rev.C to Rev.D because of the package modifications.

3 DrivExpress Package 1.1.0 — 2013.05.17

Library New Features:

- Added the `is_stop_at_err` parameter to the Simulation Control class to be able to stop Verilog simulation by occurrence of an error.
- Added the `is_stop_at_warn` parameter to the Simulation Control class to be able to stop Verilog simulation by occurrence of a warning.

Library Improvements:

- Changed to output Python script error to Verilog simulator's standard output.
- Changed to keep running Verilog simulation for the script which does not end at the `stop()` or `quit()` command of the Simulation Control class. The simulation for such a script is force-quit under previous version.

Package Modifications:

- Revised the user's manual from Rev.B to Rev.C because of supporting new features.
- Changed the installer to target all users (region independent).
- Changed the installer to install Python 2.7.3 by default.

¹ The unused lane(s) will appear when connecting to the design with multiple lanes and the actual data link is established with fewer number of lanes because of the DrivExpress license restriction.

4 DrivExpress Package 1.0.1 — 2012.12.17

The target of this package is Asian users only.

Package Modifications:

- Changed the installer to target Asian users.
- Revised the user's manual from Rev.A to Rev.B.

5 DrivExpress Package 1.0.0 — 2012.11.16

Package 1.0.0 is initial release. The target of this package is US or European users only.